

FIG. 1

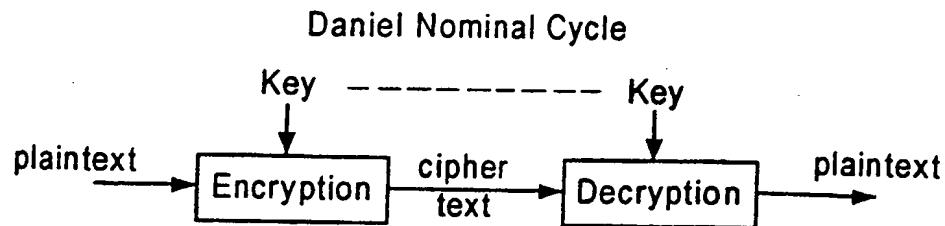
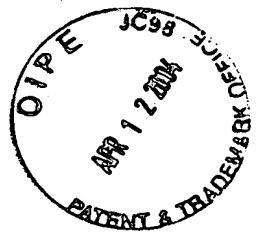


FIG. 2

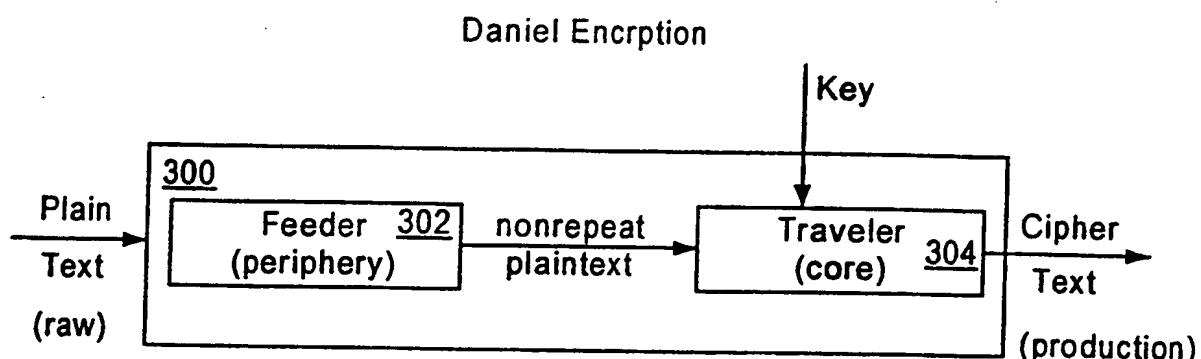


FIG. 3

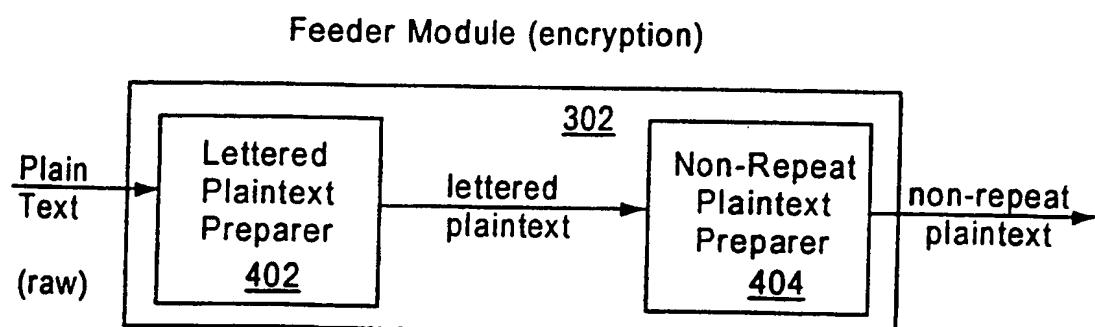


FIG. 4

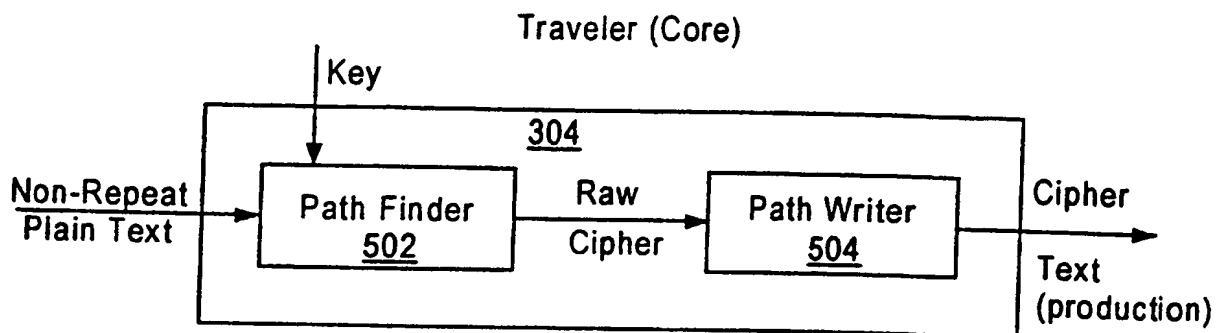
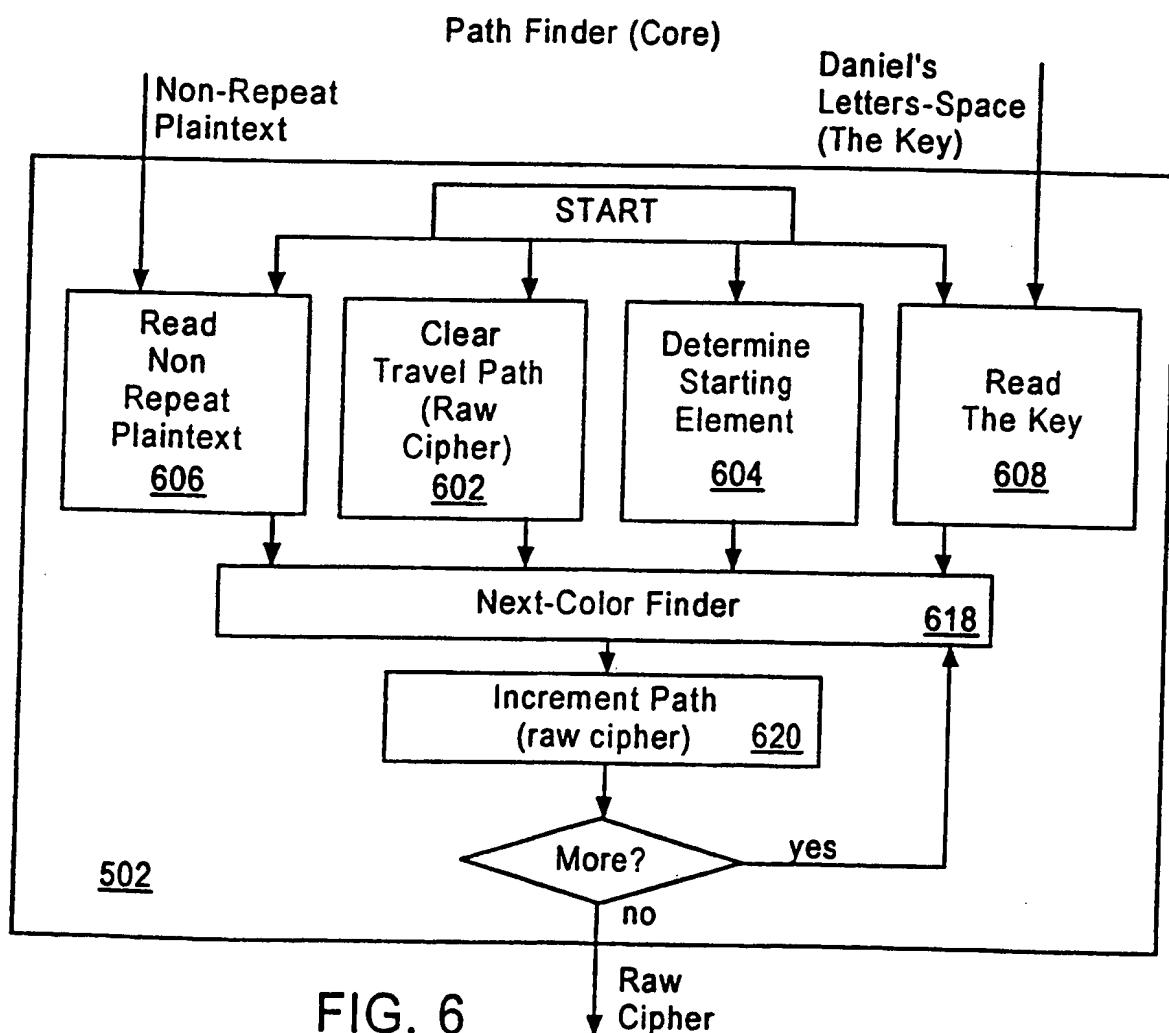


FIG. 5



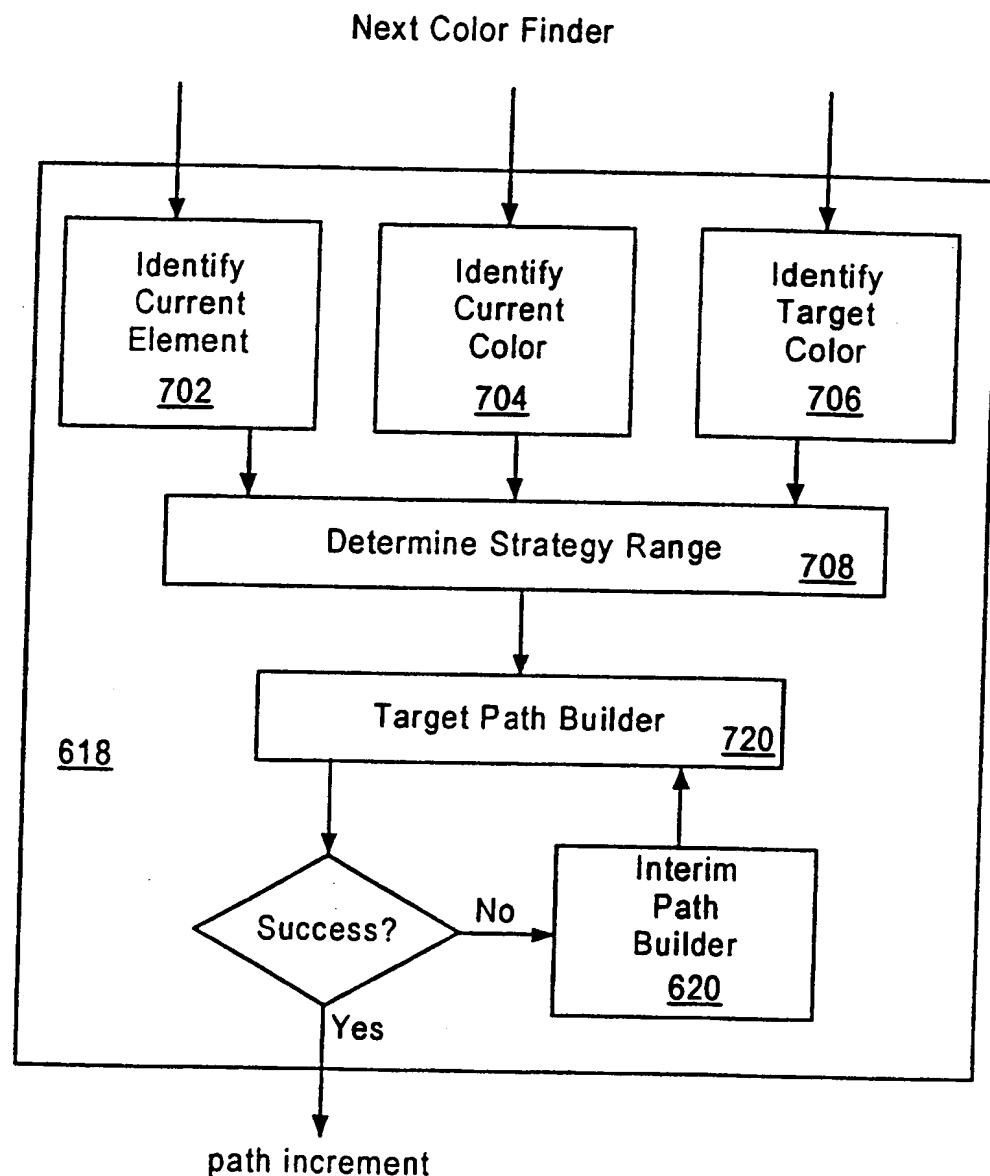


FIG. 7



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FIG. 8

Target Path Builder

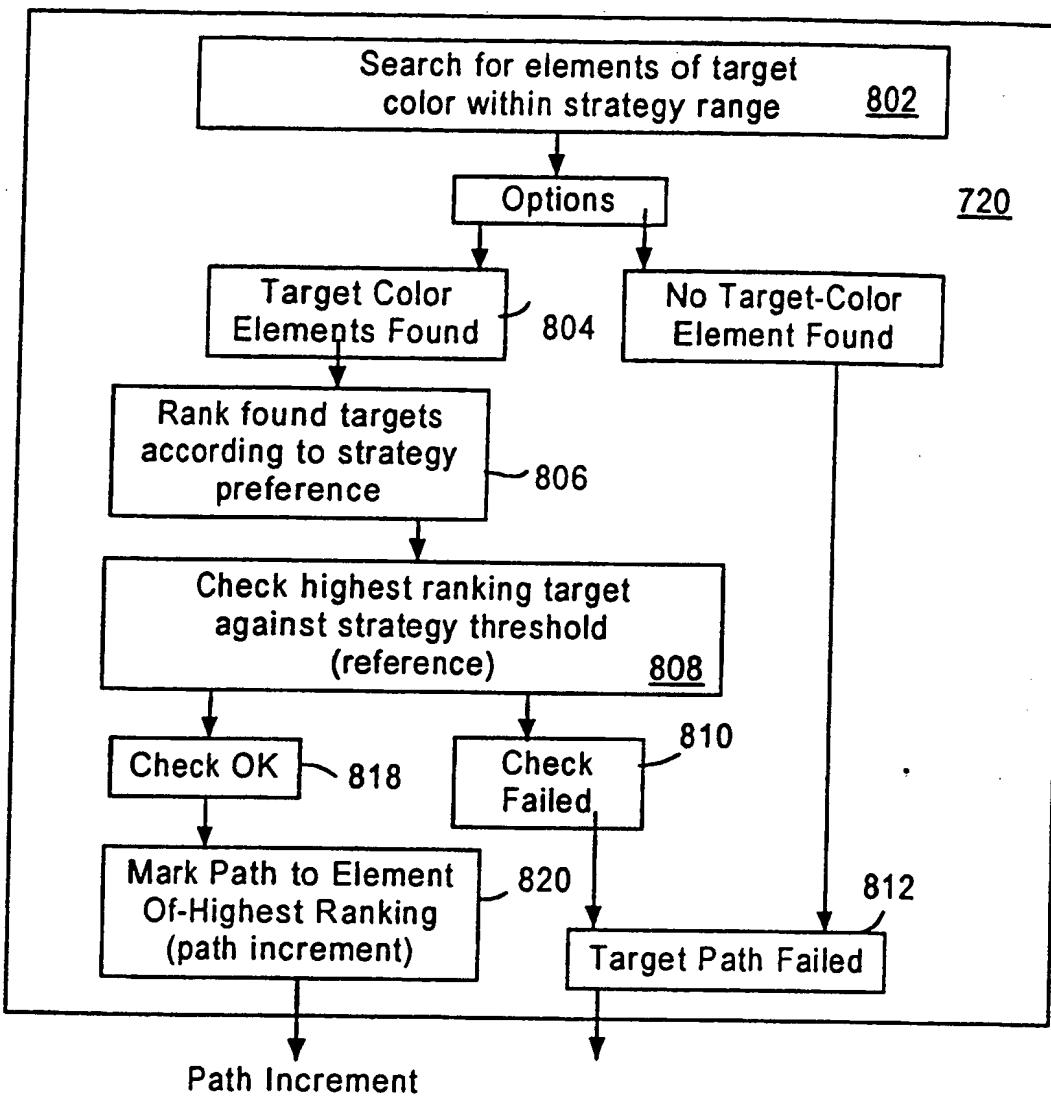
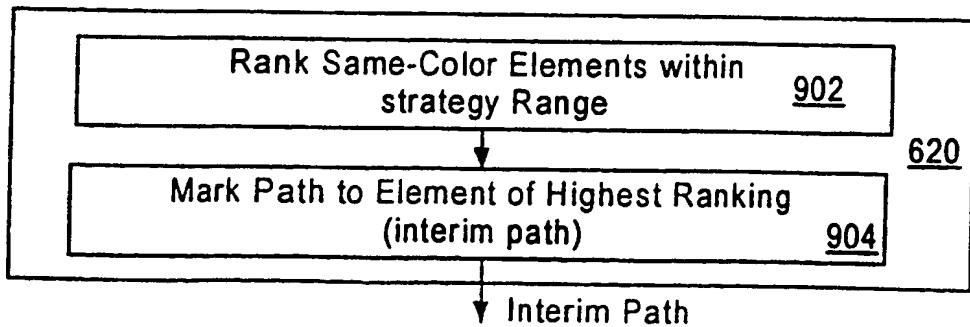


FIG. 9

Interim Path Builder





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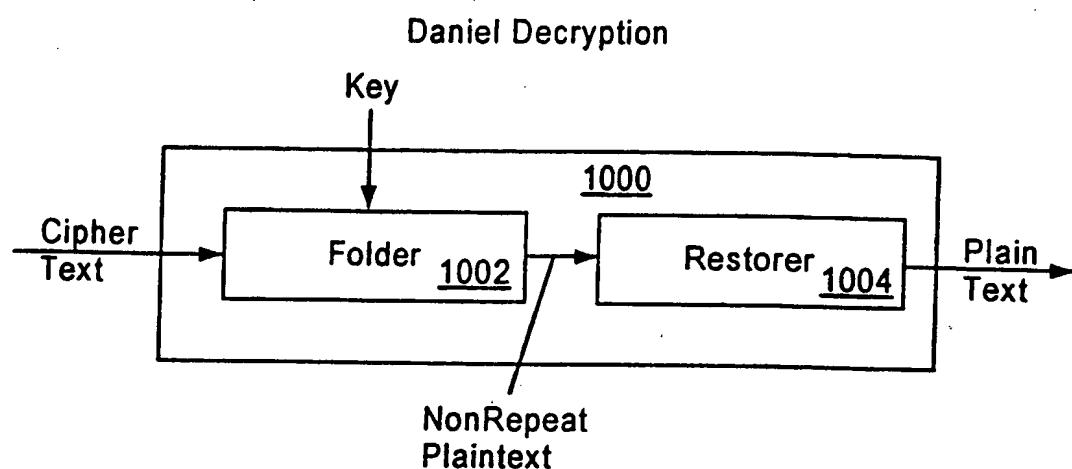


FIG. 10

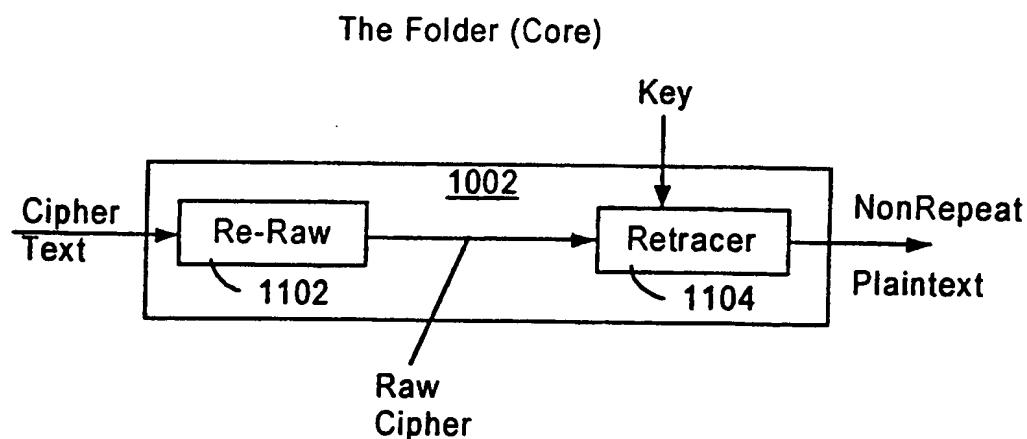
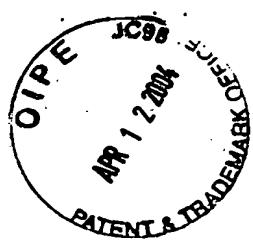


FIG. 11



Daniel Deniability Configuration

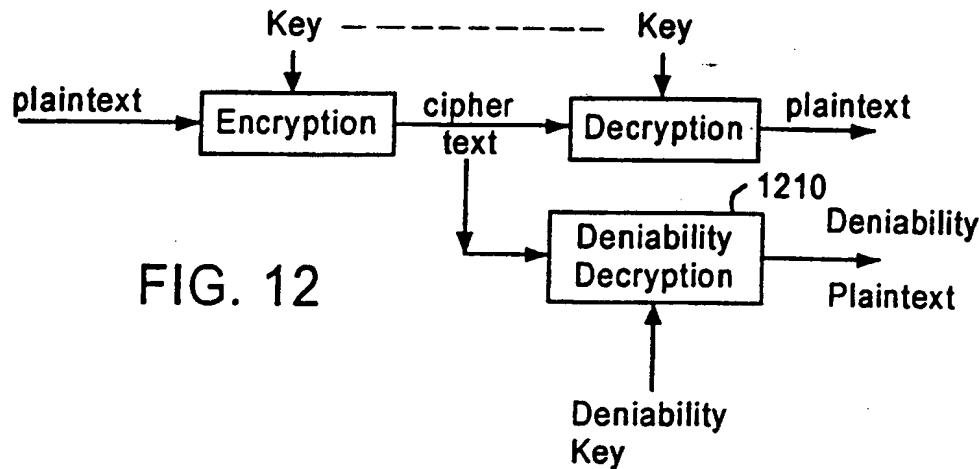


FIG. 12

Deniability Preparation

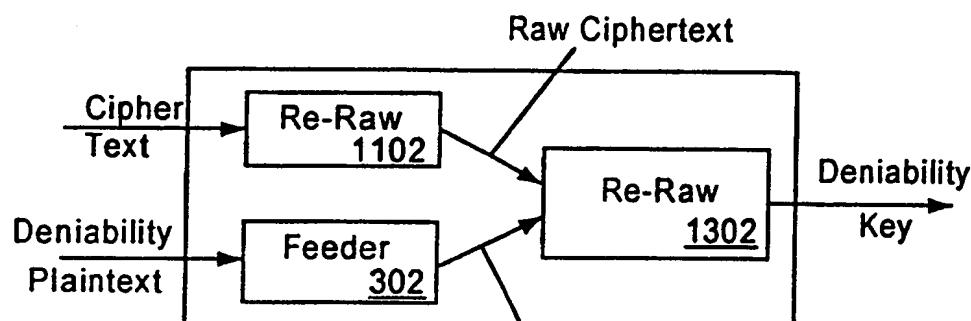


FIG. 13 Non-Repeat Plaintext

The Key Generator

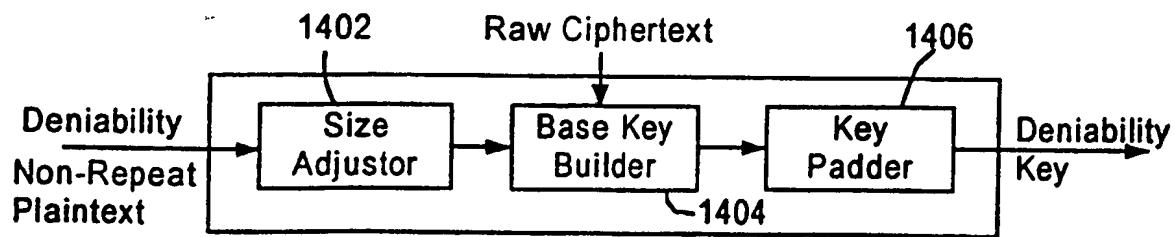
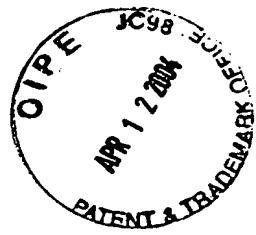


FIG. 14



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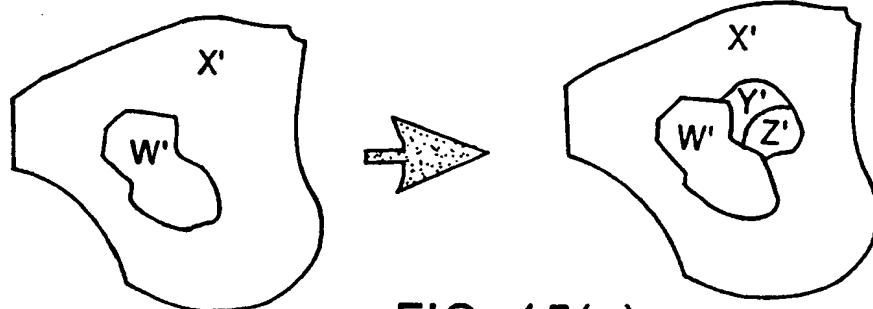


FIG. 15(a)

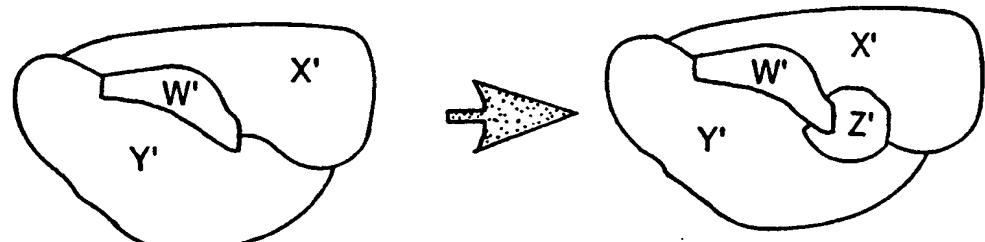


FIG. 15(b)

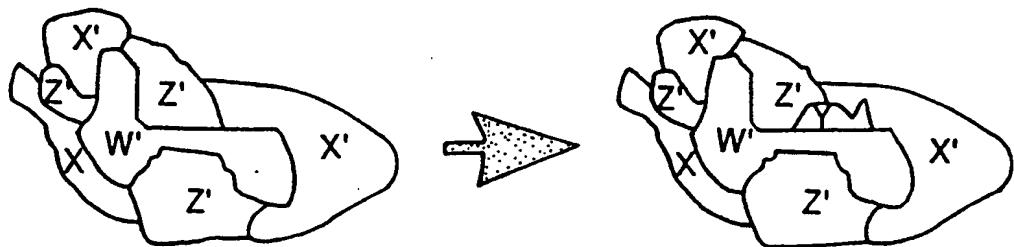
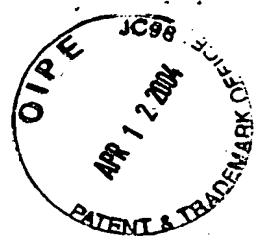
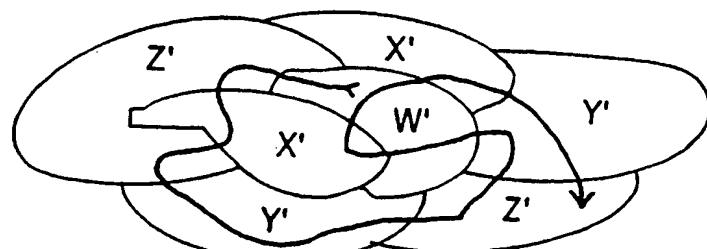


FIG. 15(c)

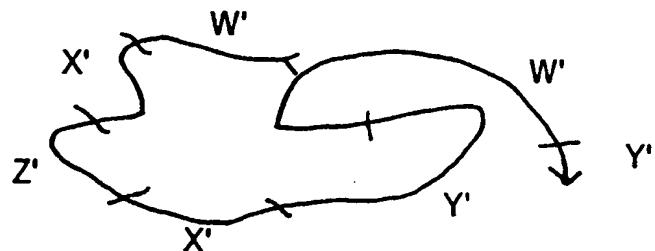


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The trail corresponds to the sequence:
W' Z' X' Z' Y' Z' Y' W' X' W' X' Y'>

FIG. 16



Mapping a sequence of choice
W' X' Z' X' Y' W' Y'
On a given cipher.

FIG. 17

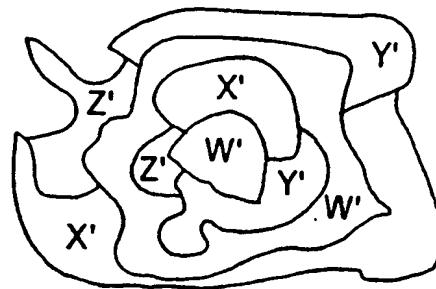
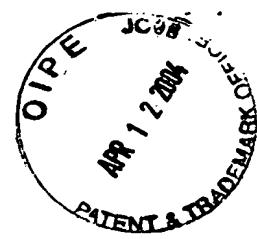


FIG. 18



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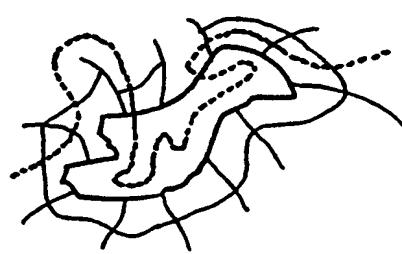


FIG. 19(a)

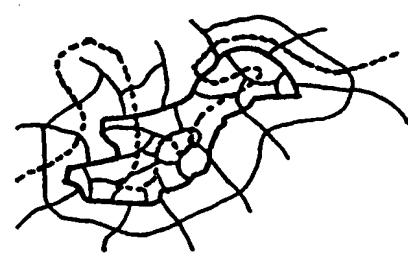
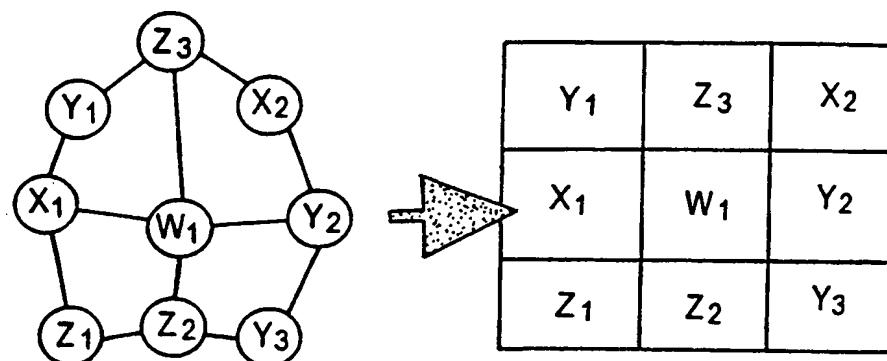


FIG. 19(b)



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Equivalence between arc-represented graph
and adjacency representation.

FIG. 20

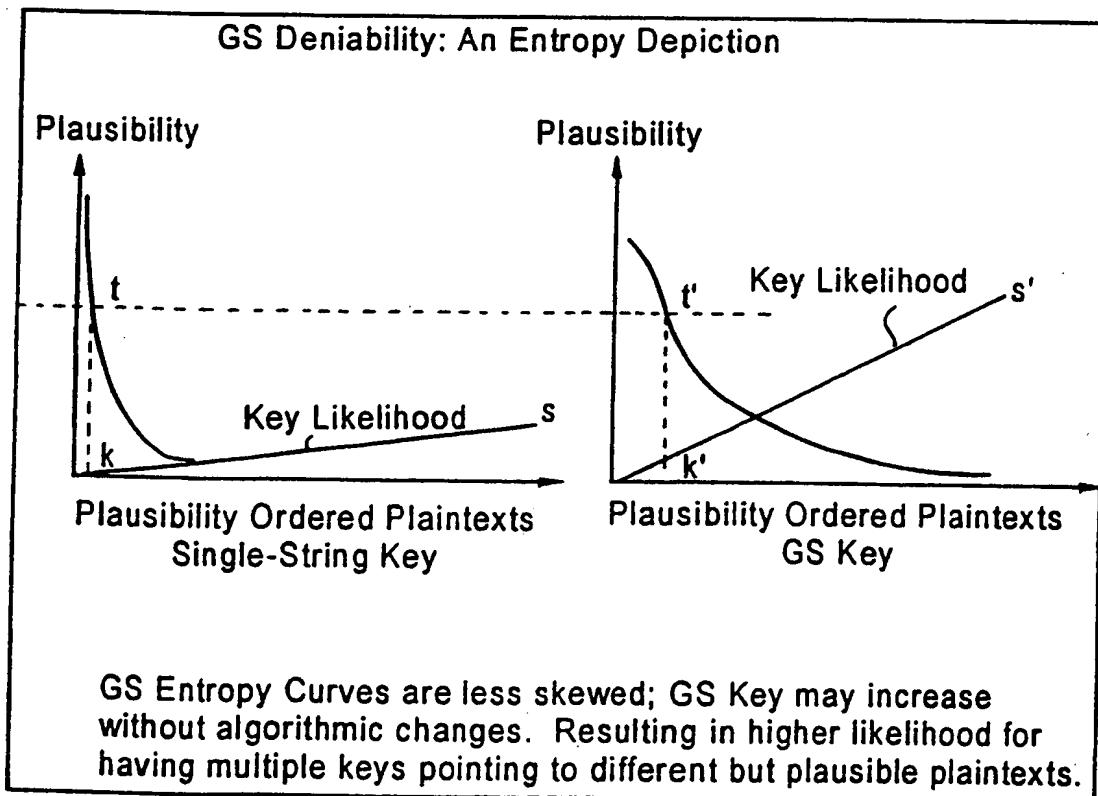


FIG. 21



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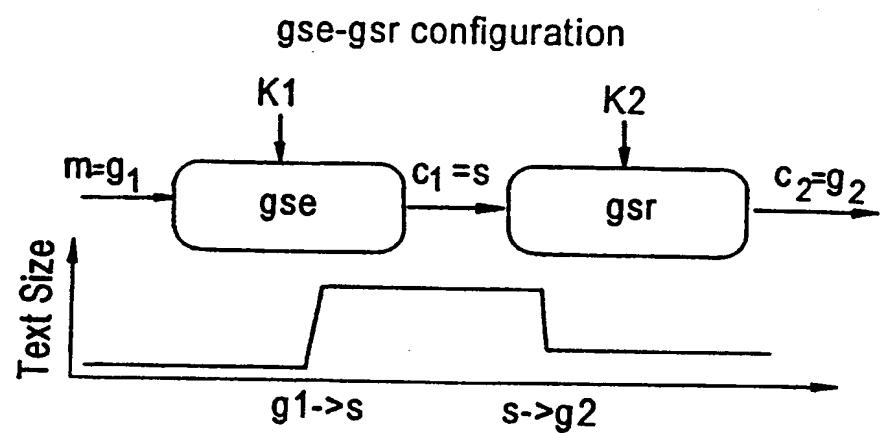


FIG. 22

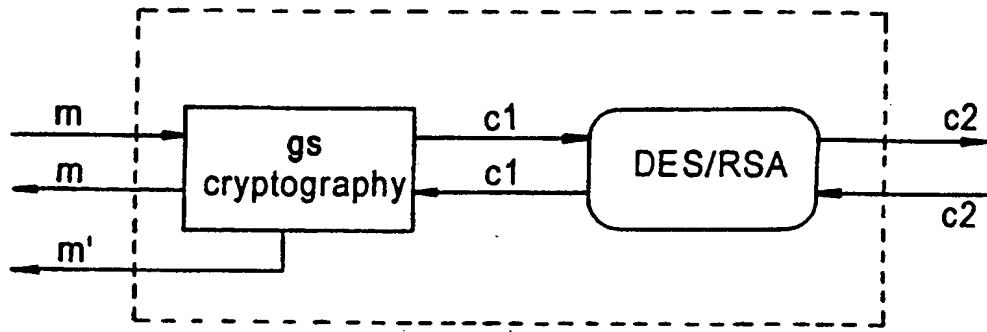
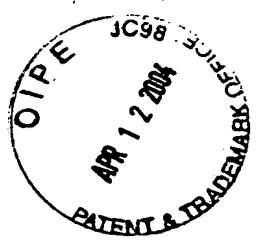


FIG. 23

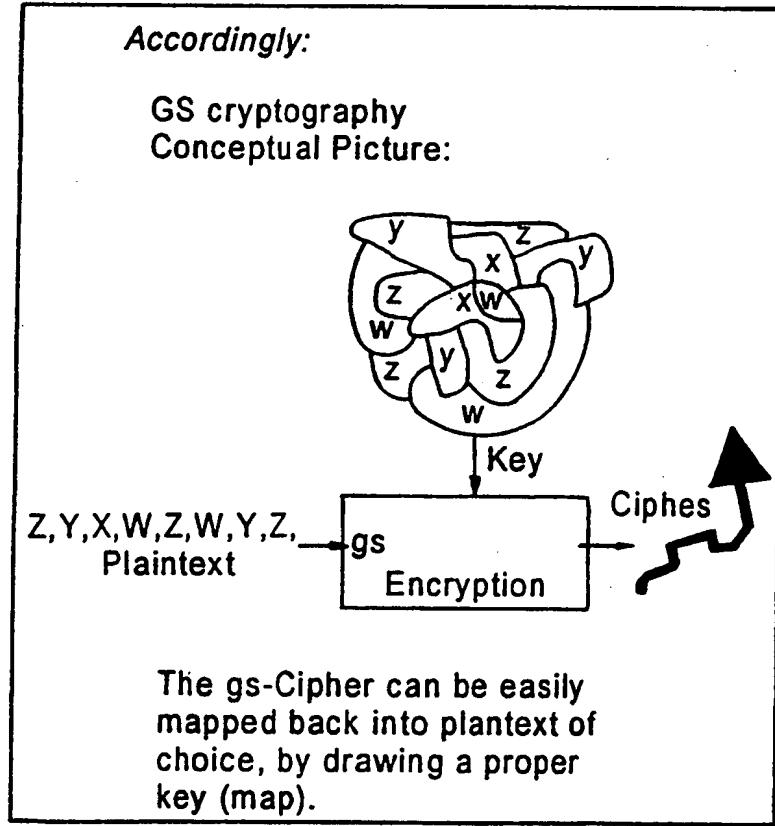
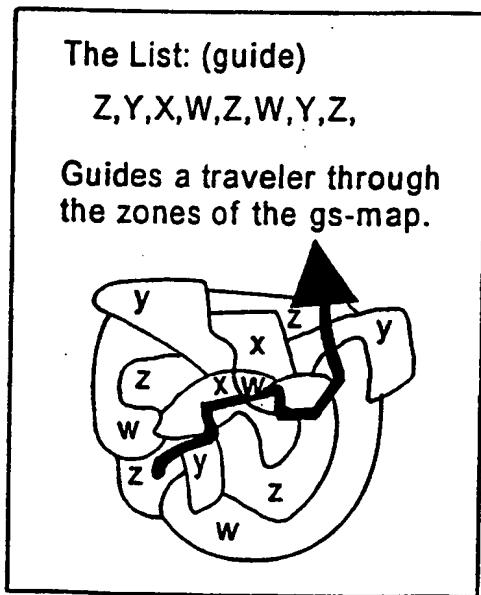


FIG. 24